

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the Application of:

MICHAEL A. JASSOWSKI

Application No.: 09/475,643

Filed: December 30, 1999

For: **OPTIMIZED DRIVER LAYOUT FOR
INTEGRATED CIRCUITS WITH
STAGGERED BOND PADS**

Art Group: 2827

Examiner: Cruz, Lourdes C.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

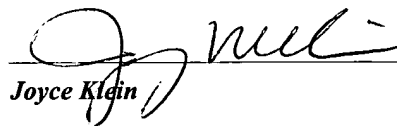


Michael J. Mallie, Reg. No. 36,591

Date: February 24, 2005

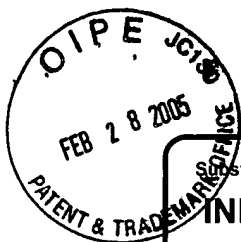
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	09/475,643
				Filing Date	December 30, 1999
				First Named Inventor	Michael A. Jassowski
				Art Unit	2827
				Examiner Name	Cruz, Lourdes C.
Sheet	1	of	2	Attorney Docket Number	42390P7143

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number Number - Kind Code² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 6,784,558	08-31-2004	Jassowski	
		US- 6,091,089	07-18-2000	Hiraga	
		US- 2003/0057549 A1	03-27-2003	Sei	
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		US- 5,581,109	12-03-1996	Hayashi, et al	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		WO 01/50526 A1	07-12-2001	Intel Corporation		
		11121498	04-30-1999	Rohm		
		03106043	05-02-1991	NEC		
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		JP401298731A	12-01-1989	NEC		
		JP405235090A	09-10-1993	NEC		

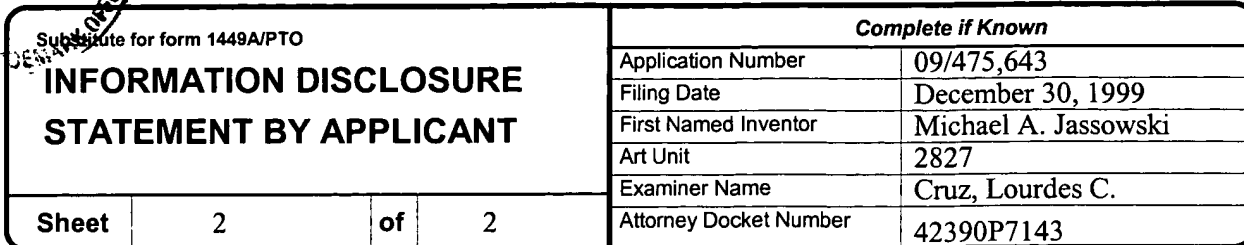
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number (optional). ‡See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. §Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ¶For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. **Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ***Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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